

PTO/SB/08A (08-03) U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>	APPLICATION NO.: 10/779,944	FILING DATE: February 17, 2004
	INVENTOR(S): Joy et al.	ART UNIT: 2154
	EXAMINER NAME: Donaghue, Larry D.	ATTY. DOCKET NO.: 188538/US/3

U.S. PATENT DOCUMENTS

EXAMINER INITIALS*	Cite No. ¹	PATENT NUMBER	ISSUE DATE	Name of Patentee of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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EXAMINER INITIALS*	Cite No. ¹	Include name of Author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), volume-issue number(s), publisher, page(s) and date.	T ⁶
/LD/	1.	Farrens et al., "Strategies for Achieving Improved Processor Throughput", ACM Sigarch, Computer Architecture, Volume 19, Number 3, pp. 362-369, May 1991.	
/LD/	2.	Bernard K. Gunther, "Multithreading with Distributed Functional Units", IEEE Transactions on Computers, Volume 46, Number 4, pp. 399-411, April 1, 1997.	
/LD/	3.	Horel et al., "UltraSPARC-III: Designing Third-Generation 64-Bit Performance", IEEE Micro, Volume 19, Number 3, pp. 73-85, May-June 1999.	
/LD/	4.	Russell et al., "Shifting Register Windows", IEEE Micro, Volume 13, Number 4, pp. 28-35, August 1993.	
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